



ACS College of Engineering
Approved by AICTE New Delhi, Affiliated to VTU, Belagavi
(A Unit of RajaRajeswari Group of Institutions)



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Report on Technical Talk ---Career opportunities in VLSI Technology

Name of the Event	:	Technical Talk
Date & Time:		24.08.2019&10:45AM
Venue:		Seminar Hall 1
Participants:		ECE students
Resource Person:		Mr. K.G. Managond, Senior Physical design Engineer, Altron Technologies, Bangalore

About the Talk

The Technical talk started with welcoming of Mr.K.G. Managond by presenting a bouquet by Dr.M. Mathivanan.

The Speaker started the session with the basics of VLSI, its design and the present technology in industries. Also, the gap between the curriculum syllabus and industry was also discussed. The Speaker also gave an idea what basics to concentrate on to clear the technical interview in VLSI industries. In Industries, fabrication of IC's, its processing and the technology used is explained in detail by the speaker. Finally, the career opportunities related to verification system Engineer, Layout Engineer, Physical designer Engineer requirements are discussed.

The session was very much helpful to students as the interaction with speaker went on very well.



Resource Person Mr. K G Managond with Dr.M. Mathivanan



Interaction with Students.

